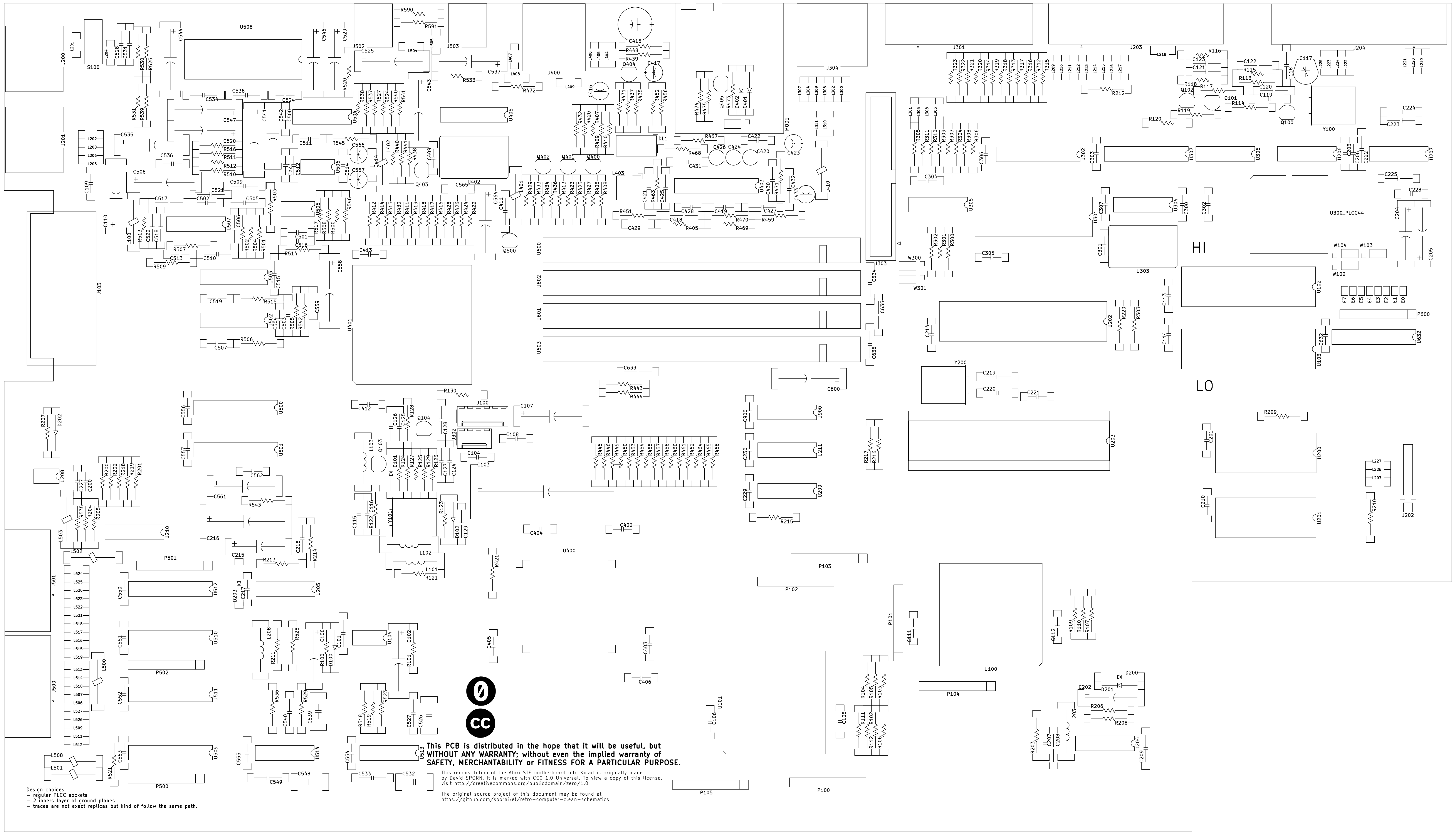

Design choices
- regular PLCC sockets
- 2 inners layer of ground planes
- traces are not exact replicas but kind of follow the same path.

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The original source project of this document may be found at https://github.com/sporniket/retro-computer-clean-schematics

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Reference : C300780-001

Sheet:
File: motherboard.kicad_pcb

Title: Converted schematics of Atari STE

Size: A2	Date: 2021-07-18	**Rev: 1.0.0.rc3**
KiCad E.D.A.	kicad 5.1.10-88a1d61d5888ubuntu18.04.1	Id: 1/1