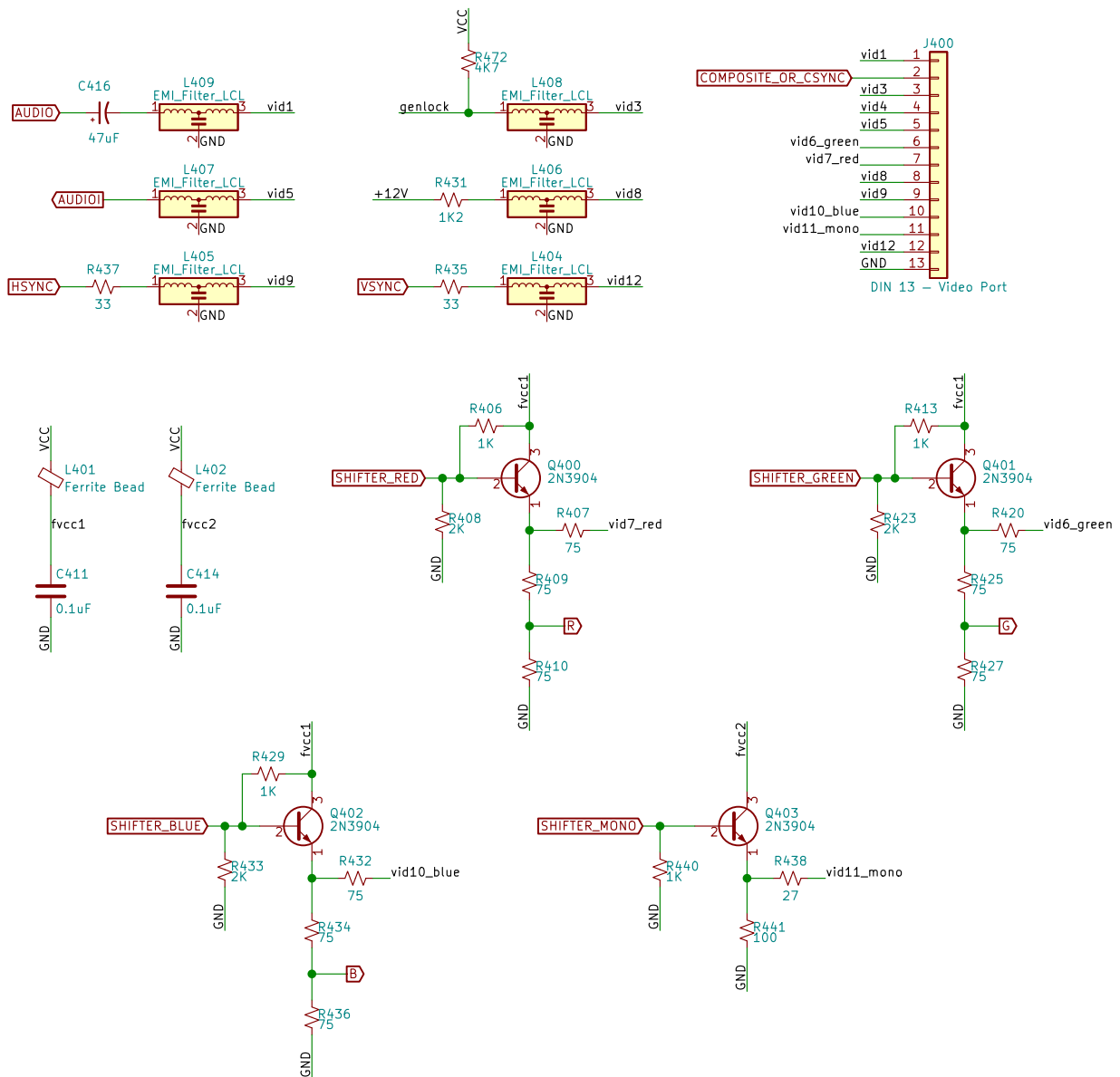


Genlock is active low.
 -> When asserted (low), vid4 is the external video clock of the genlock, and sent as the main clock MC to the shifter ; MONOMON is forced to high (color display).
 -> When negated (high), the 32MHz clock is sent as the main clock MC to the shifter ; vid4 is the detection of monochrome displays and sent as MONOMON.

This conversion of the Atari STE schematics into Kicad is originally made by David SPORN. It is marked with CC0 1.0 Universal. To view a copy of this license, visit <http://creativecommons.org/publicdomain/zero/1.0>

The original source project of this document may be found at <https://github.com/sporniket/retro-computer-clean-schematics>



Sheet: /J400 - Video Port/
 File: subsheet_402.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 5.1.10-88a1d61d5888ubuntu18.04.1

Rev:

Id: 21/21