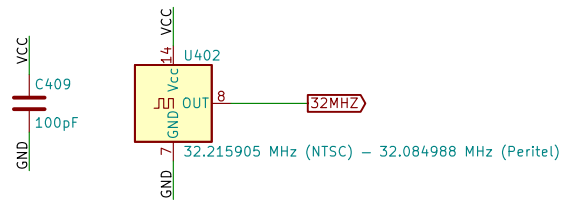


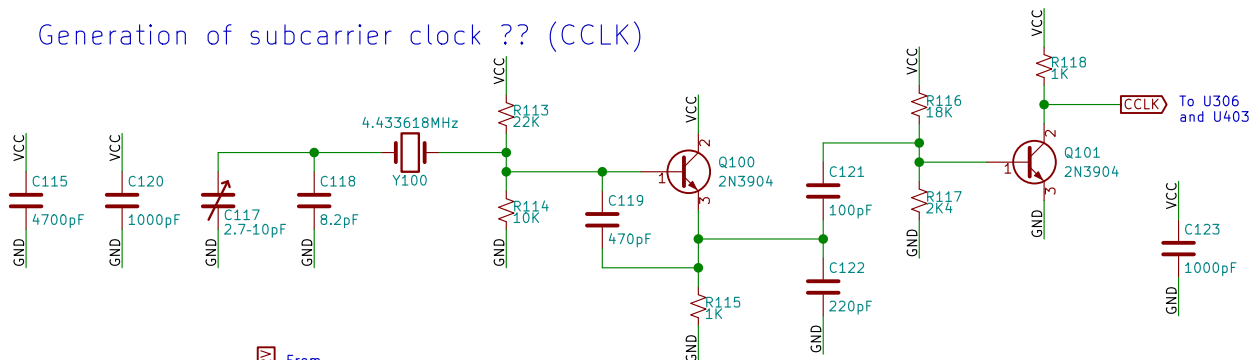
Installed ONLY in non-PAL systems



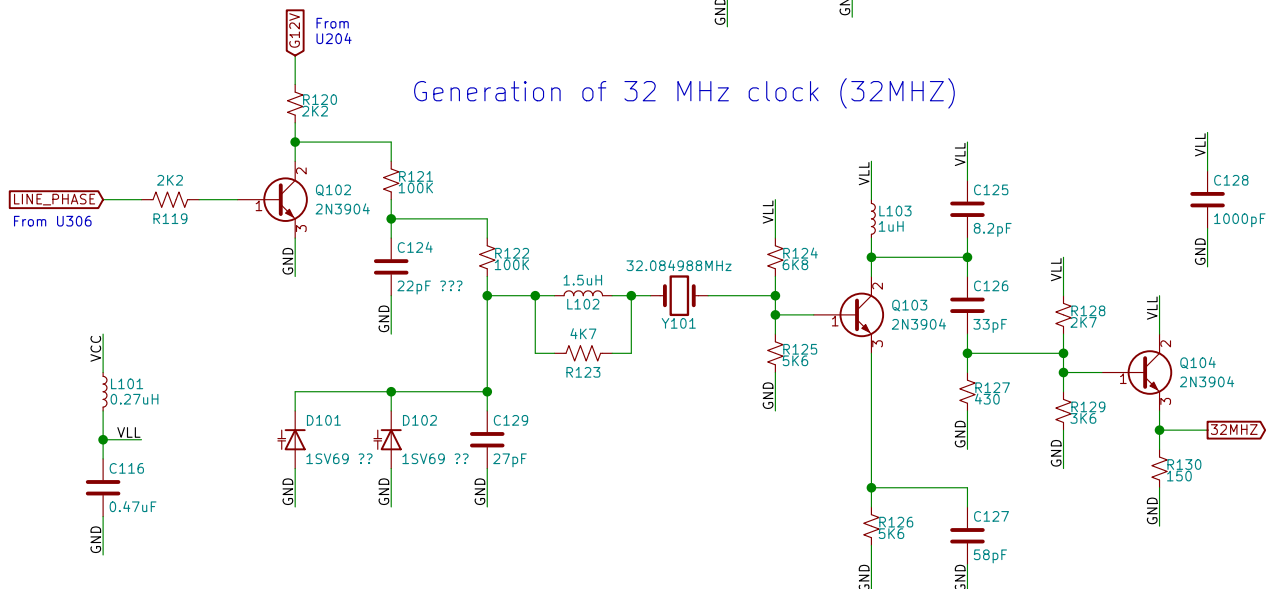
Installed ONLY in PAL systems

To have an idea of what this circuitry is for, see <https://en.wikipedia.org/wiki/PAL>

Generation of subcarrier clock ?? (CCLK)



Generation of 32 MHz clock (32MHZ)



Sheet: /Sheet 1 of 6/32 MHz clock and video clocks/
File: subsheet_106.sch

Title:

Size: A4

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